



CLAIM AMENDMENTS

1. (Currently Amended) A process of generating a software tool that causes a computer to create~~adjusts~~ computer files that define integrated circuits as a plurality of~~in a first computer-readable directory structure to computer files that define the integrated circuits in a second computer-readable~~ directories in a second directory structure based on a second version of an ASIC design system, ~~each directory of the directory structure having a source name~~, the process comprising steps of:

a) providing a plurality of first computer-readable directories arranged in a first directory structure based on a first version of an ASIC design system different from the second version;

b) comparing the first and second computer-readable directories to identify differences between the first and second~~computer-readable directory structures;~~

c) generating a computer-readable map file containing a plurality of items, each referencing a difference between the first and second computer-readable directory structures by an associated source name;

d) sorting the items of the mapping file into an ordered list based on the source names;

e) for each source name, generating a computer-readable code representing a difference between the first and second computer-readable directory structures associated with the respective source name; and

f) generating the software tool based on the computer-readable codes, the software tool being executable by a computer to cause the computer to respond to computer files that define the integrated circuit in the first directory structure to generate the computer files that define the integrated circuit in the second directory structure.

2. (Currently Amended) The process of claim 1, wherein step (c) ~~includes further comprises~~ steps of:

- c1) sorting the source names by length, and
- c2) after step (c1), alphanumerically sorting source names of equal length.

3. (Currently Amended) The process of claim 2, further ~~including comprising~~ after step (c) and before step (d)

- f) parsing the source names.

4. (Currently Amended) The process of claim 3, further ~~including comprising~~ steps of:

- f) packaging the tool with the second ~~computer-readable~~ directory structure.

5. (Currently Amended) The process of claim 2, further ~~including comprising~~ steps of:

- f) packaging the tool with the second ~~computer-readable~~ directory structure.

6. (Currently Amended) The process of claim 1, further ~~including comprising~~ after step (c) and before step (d)

- f) parsing the source names.

7. (Currently Amended) The process of claim 6, further ~~including comprising~~ steps of:

- f) packaging the tool with the second ~~computer-readable~~ directory structure.

8. (Currently Amended) A computer process of ~~converting a first~~generating a second computer file that defines~~file defining~~ an integrated circuit in a second computer-readable directories of a second directory structure based on a second version of an

ASIC design system using a first computer file that defines the integrated circuit in first computer-readable directories of a first directory structure based on a first version of an ASIC design system different from the second version,~~first computer-readable directory structure to a second computer file,~~ wherein the first computer file contains a plurality of lines, at least some of which contain one or more source names referencing first computer-readable directories of the~~a first computer-readable directory structure,~~ the process comprising steps of:

a) providing a computer containing a software tool that maps directory references between the first ~~computer-readable~~ directory structure and ~~a the second computer-readable~~ directory structure by source name;

b) input the first computer file to the computer;

c) identifying source names in a line of the first computer file that ~~is~~are referenced by the software tool;

d) for each identified source name, ~~changing~~substituting ~~a second the associated directory associated with the identified source name for each reference from the first computer-readable directory associated with the identified source name~~structure to the second computer-readable directory structure.

9. (Original) The process of claim 8, wherein step (c) is performed by steps of:

c1) comparing the source names in the line to the source names identified by the software tool, and

c2) selecting the directory reference in the first computer file associated with each source name in the line that matches a source name identified by the software tool.

10. (Currently Amended) The process of claim 9, including further comprising steps of:

e) storing the line containing a ~~changed~~substituted

directory reference to ~~a~~the second computer file, and

f) repeating steps (c) and (d) for each line in the first computer file.

11. (Currently Amended) The process of claim 10, ~~including~~further comprising:

g) output the second computer file as defining the integrated circuit in the second ~~computer-readable~~ directory structure.

12. (Currently Amended) The process of claim 8, ~~including~~further comprising steps of:

e) storing the line containing a changed directory reference to ~~a~~the second computer file, and

f) repeating steps (c) and (d) for each line in the first computer file.

13. (Currently Amended) The process of claim 12, ~~including~~further comprising:

g) output the second computer file as defining the integrated circuit in the second ~~computer-readable~~ directory structure.

14. (Original) The process of claim 8, wherein the second computer file overwrites the first computer file.

15. (Currently Amended) A computer useable medium having a computer-readable ~~program~~software tool embodied therein for controlling a computer containing a first computer-readable file that defines an integrated circuit in a first ~~computer-readable~~ directory structure containing first computer-readable directories based on a first version of an ASIC design system to ~~convert the first computer-readable file to~~create a second

computer-readable file defining the integrated circuit in a second computer-readable directories of a second directory structure ~~computer-readable~~ based on a second version of an ASIC design system different from the first version, ~~directory structure, wherein~~ the first computer-readable file contains a plurality of lines, at least some of which contain one or more source names referencing directories of the first ~~computer-readable~~ directory structure, the computer-readable program software tool comprising:

first computer-readable program code for causing the computer to identify source names in each line of the first computer-readable file; and

second computer-readable program code for causing the computer to ~~change~~ substitute the directory reference associated with each source name from the first ~~computer-readable~~ directory structure to the second ~~computer-readable~~ directory structure.

16. (Currently Amended) The computer useable medium of claim 15, wherein the computer-readable ~~program~~ software tool includes computer-readable data representing source names in the first and second computer-readable directory structures, and the first computer-readable program code ~~includes~~ further comprises:

computer-readable program code for causing the computer to compare the source names in the line of the first computer-readable file to the source names represented by the computer-readable data, and

computer-readable program code for causing the computer to select the directory reference in the first computer-readable file associated with each identified source name in the line.

17. (Currently Amended) The computer useable medium of claim 16, wherein the computer-readable ~~program~~ software tool ~~includes~~ further comprises:

computer-readable program code for causing the computer to store the line containing a changed directory reference to a the second computer-readable file.

18. (Currently Amended) The computer useable medium of claim 17, wherein the computer-readable programsoftware tool includesfurther comprises:

computer-readable program code for causing the computer to output the second computer-readable file as defining the integrated circuit in the second ~~computer-readable~~ directory structure.

19. (Currently Amended) The computer useable medium of claim 15, wherein the computer-readable programsoftware tool includesfurther comprises:

computer-readable program code for causing the computer to store the line containing a changed directory reference to a second computer-readable file.

20. (Currently Amended) The computer useable medium of claim 19, wherein the computer-readable programsoftware tool includesfurther comprises:

computer-readable program code for causing the computer to output the second computer-readable file as defining the integrated circuit in the second ~~computer-readable~~ directory structure.